

BIOGRAPHY

Anurag K. Srivastava is an associate professor of electric power engineering at Washington State University and the director of the Smart Grid Demonstration and Research Investigation Lab (SGDRIL) within the Energy System Innovation Center (ESIC). He received his Ph.D. degree in electrical engineering from the Illinois Institute of Technology in 2005. In past years, he has worked in different capacity at the PEAK RC, Réseau de transport d'électricité in France; RWTH Aachen University in Germany; Idaho National Laboratory, Pacific Northwest National Lab, PJM Interconnection, Schweitzer Engineering Lab (SEL), GE Grid Solutions, Massachusetts Institute of Technology and Mississippi State University in USA; Indian Institute of Technology Kanpur in India; as well as at Asian Institute of Technology in Thailand. His research interest includes data-driven algorithms for power system operation and control including resiliency analysis. Dr. Srivastava research efforts resulted in more than \$50M high impact projects resulted in tools installed at the utility control center supported by US Department of Energy, National Science Foundation, Siemens Corporate Research, Electric Power Research Institute, Schweitzer Engineering Lab, Power System Engineering Research Center, Office of Naval Research and several National Labs. He is a senior member of the IEEE, vice-chair of the IEEE Power & Energy Society's (PES) PEEC committee, co-chair of the microgrid working group, secretary of power system operation SC, vice-chair of PES voltage stability working group, chair of PES synchrophasors applications working group, past-chair of the IEEE PES career promotion subcommittee, past-chair of the IEEE PES student activities committee, and past vice-chair of the IEEE synchrophasor conformity assessment program. He organized NSF sponsored Data analytics workshop for the power grid resiliency in 2018, Siemens sponsored data analytics in the smart grid workshop in 2017, North American Power Symposium in 2014, and IEEE sponsored workshop on Testing and validation of synchrophasor devices and applications in 2012. Dr. Srivastava is an editor of the IEEE Transactions on Smart Grid, IEEE Transactions on Power Systems, IET Generation, Transmission and Distribution and Elsevier Sustainable Computing. He is an IEEE distinguished lecturer and has delivered 30+ keynotes/ tutorial in more than 15 countries. He is author of more than 300 technical publications including a book on power system security and 3 patents.